4V Drive Nch+Nch MOSFET

SP8K2

Structure

Silicon N-channel MOSFET

Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small surface Mount Package (SOP8).

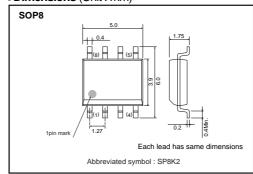
Application

Power switching, DC / DC converter.

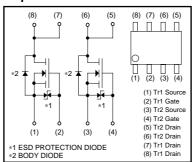
Packaging specifications

	Package	Taping
Туре	Code	ТВ
	Basic ordering unit (pieces)	2500
CDOKO	•	

●Dimensions (Unit: mm)



●Equivalent circuit



the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

● Absolute maximum ratings (Ta=25°C) <It is the same ratings for the Tr1 and Tr2.>

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V _{DSS}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	Continuous	ID	±6.0	Α	
	Pulsed	I _{DP} *1	±24	Α	
Source current (Body diode)	Continuous	Is	1.6	Α	
	Pulsed	I _{SP} *1	6.4	Α	
Total power dissipation		P _D *2	2	W	
Channel temperature		Tch	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 MOUNTED ON A CERAMIC BOARD.

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	62.5	°C/W

^{*}MOUNTED ON A CERAMIC BOARD.

●Electrical characteristics (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μА	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	30	_	_	٧	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	_	1	μΑ	V _{DS} =30V, V _{GS} =0V
Gate threshold voltage	VGS (th)	1.0	_	2.5	V	VDS=10V, ID=1mA
Static drain-source on-state resistance	R _{DS (on)} *	-	21	30	mΩ	I _D =6.0A, V _{GS} =10V
		_	30	42		I _D =6.0A, V _{GS} =4.5V
		_	33	47		I _D =6.0A, V _{GS} =4V
Forward transfer admittance	Y _{fs} *	4.0	_	_	S	ID=6.0A, VDS=10V
Input capacitance	Ciss	_	520	_	pF	V _{DS} =10V
Output capacitance	Coss	_	150	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	95	-	pF	f=1MHz
Turn-on delay time	td (on) *	_	9	_	ns	ID=3A, VDD≒15V
Rise time	tr *	_	21	_	ns	V _{GS} =10V
Turn-off delay time	td (off) *	_	36	_	ns	R _L =5Ω
Fall time	t _f *	-	13	_	ns	$R_G=10\Omega$
Total gate charge	Qg *	_	7.2	10.1	nC	V _{DD} ≒15V
Gate-source charge	Q _{gs} *	_	1.8	_	nC	V _{GS} =5V
Gate-drain charge	Q _{gd} *	-	2.8	_	nC	I _D =6.0A

^{*}Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C) < It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp*	_	_	1.2	V	Is=6.4A. Vgs=0V

^{*}Pulsed

Electrical characteristic curves

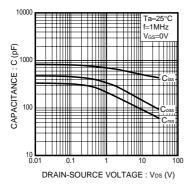


Fig.1 Typical Capacitance vs. Drain-Source Voltage

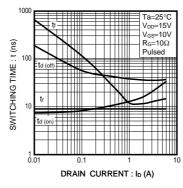


Fig.2 Switching Characteristics

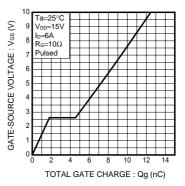


Fig.3 Dynamic Input Characteristics

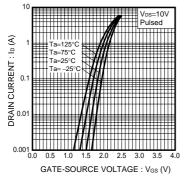


Fig.4 Typical Transfer Characteristics

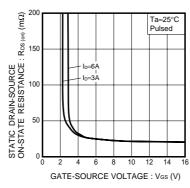


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

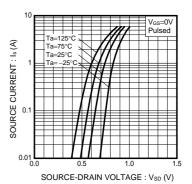


Fig.6 Source Current vs. Source-Drain Voltage

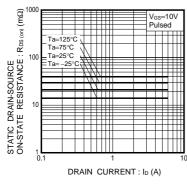


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

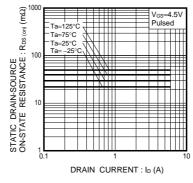


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

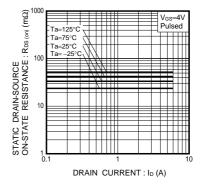


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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ROHM CO., LTD. 21, Saiin Mizosaki-cho, Ukyo-ku, Kyoto 615-8585, Japan

pan TEL:+81-75-311-2121 FAX:+81-75-315-0172

